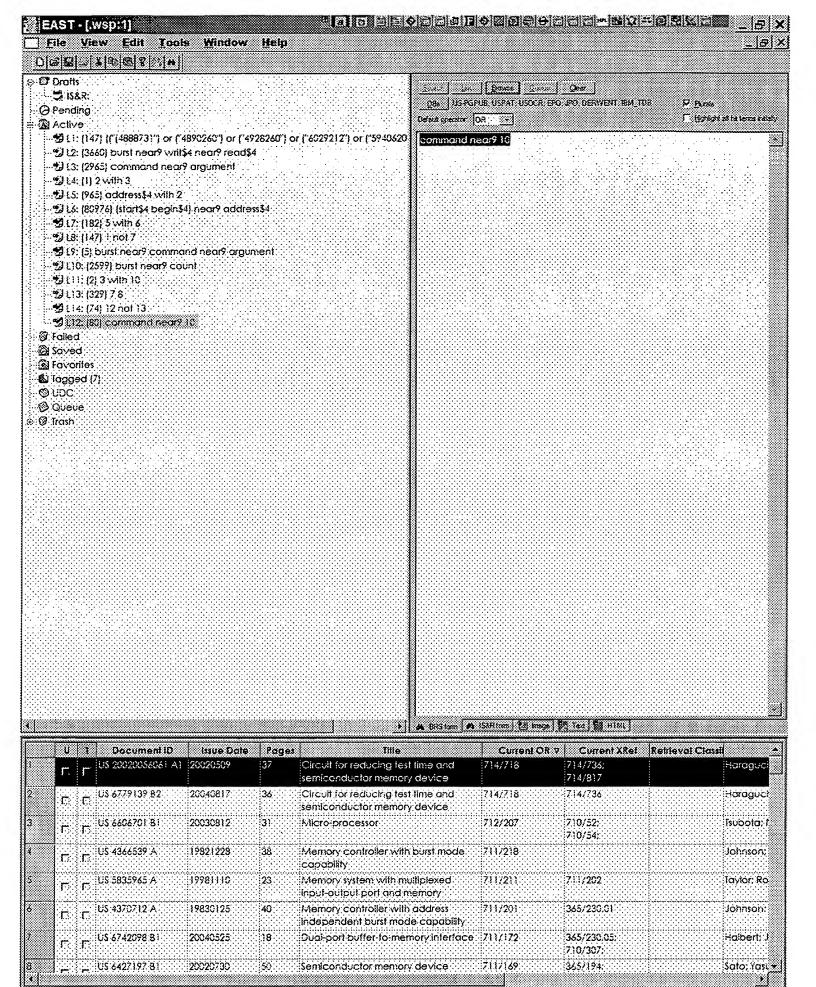


4 US 20050243568 A1 20051103	Set of parts and assembling method 362/494 Rodrigue:
	for assembling a rear-view side mirror : at:
\$ US 20050227810 AT 20051013	12 Drive device for working vehicle and 477/172 Nakagax
	method of controlling the drive of
6 US 20050225994 A1 20051013	Rear-view mirror with a reflected 362/494 Rodrigue
	combined signal and optics at
7 US 20050206585 A1 20050922	Display devices and integrated 345/34 Stewart.
	Circuits:
# US 20050165862 A1 20050728	9 Autonomic and fully recovering 707/202 Loaiman
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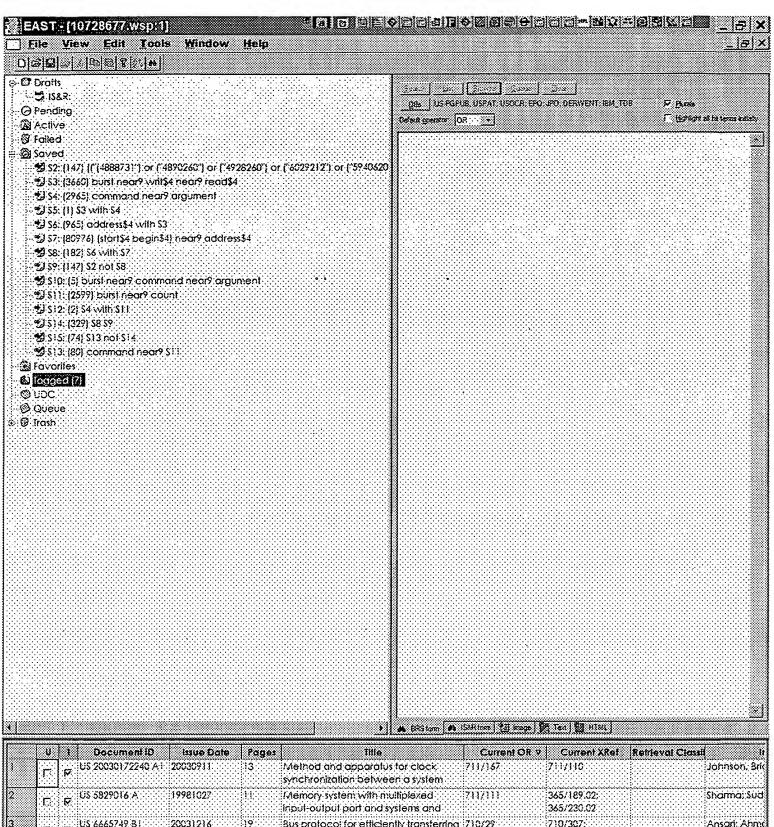
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	U	1	Document ID	issue Date	Pages	Title	Current OR 9	Current XRef	Retrieval Classi	1
	Γ	Þ	US 20030172240 A1	20030911	13	Method and apparatus for clock 71 synchronization between a system	1/167	711/110		Johnson, Bri
2	Г	₹	US 5829016 A	19981G27	11	Memory system with multiplexed 71 input-output part and systems and		365/189.02; 365/230.02		Sharma; Sud
3	г	Þ	US 6665749 81	20031216	J Ģ	Bus protocol for efficiently transferring 710 vector data		710/307: 710/33;		Ansari: Ahmi
•	г	F	US 5644784.A	19970701	13	Linear list based DMA control structure 71(0/24	710/35		Peek; Grego
5	Γ	Þ	US 5765022 A	19980609	12	System for transferring data from a 710 source device to a target device in	367 77 010101010101010101	7 0/26; 7 0/33;		Kalser; John
6	G	₽.	US 6229759 81	20010508	16	Semiconductor memory burst length 365 count determination method		365/189.05; 365/233;		Morgan; Do
7	r	P	US 5347634 A	19940913	12	System and method for directly. 34: executing user DMA instruction from	5/539	222/500		Herreil: Russ
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